



Applicants: Paul Metzgen et al. Docket No.: ALT-291

Application No.: 10/728,645

Filed: December 4, 2003

TC Art Unit: 2819

Examiner: Vibol Tan

For:

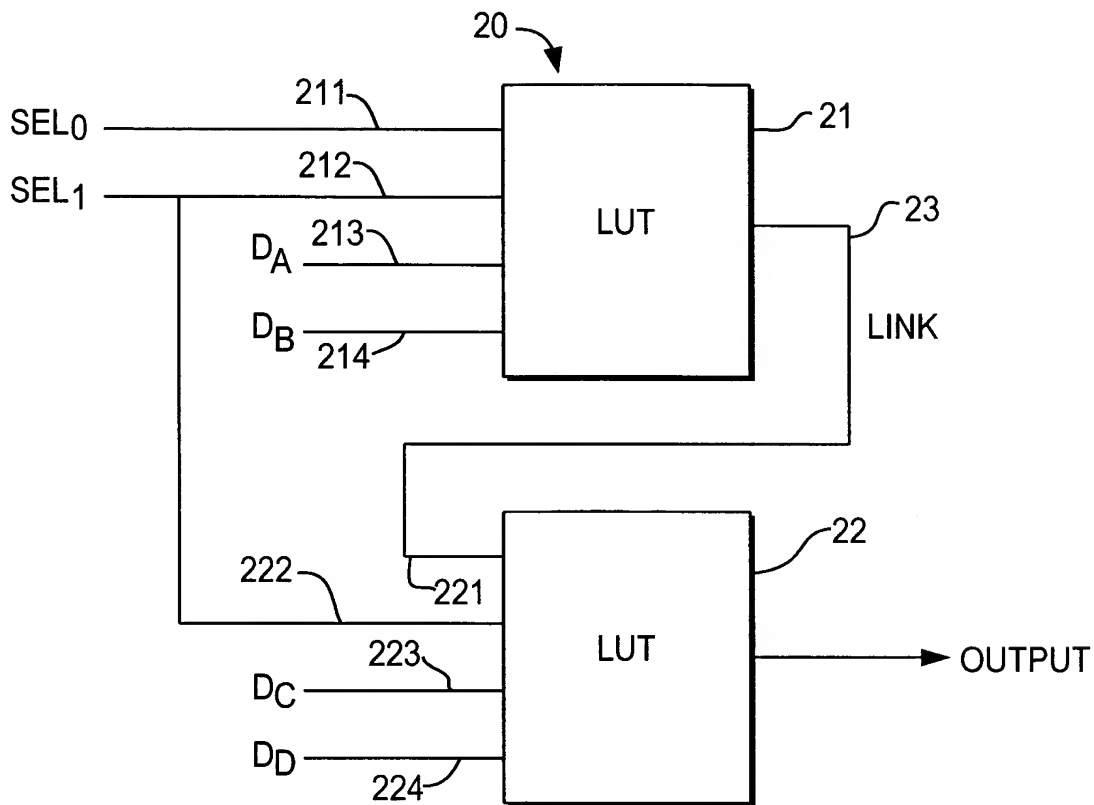
MULTIPLEXER CONFIGURATION FOR PROGRAMMABLE LOGIC DEVICE

Attorney:

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Replacement Sheet 2 of 7

2 / 7



**FIG. 2**  
PRIOR ART